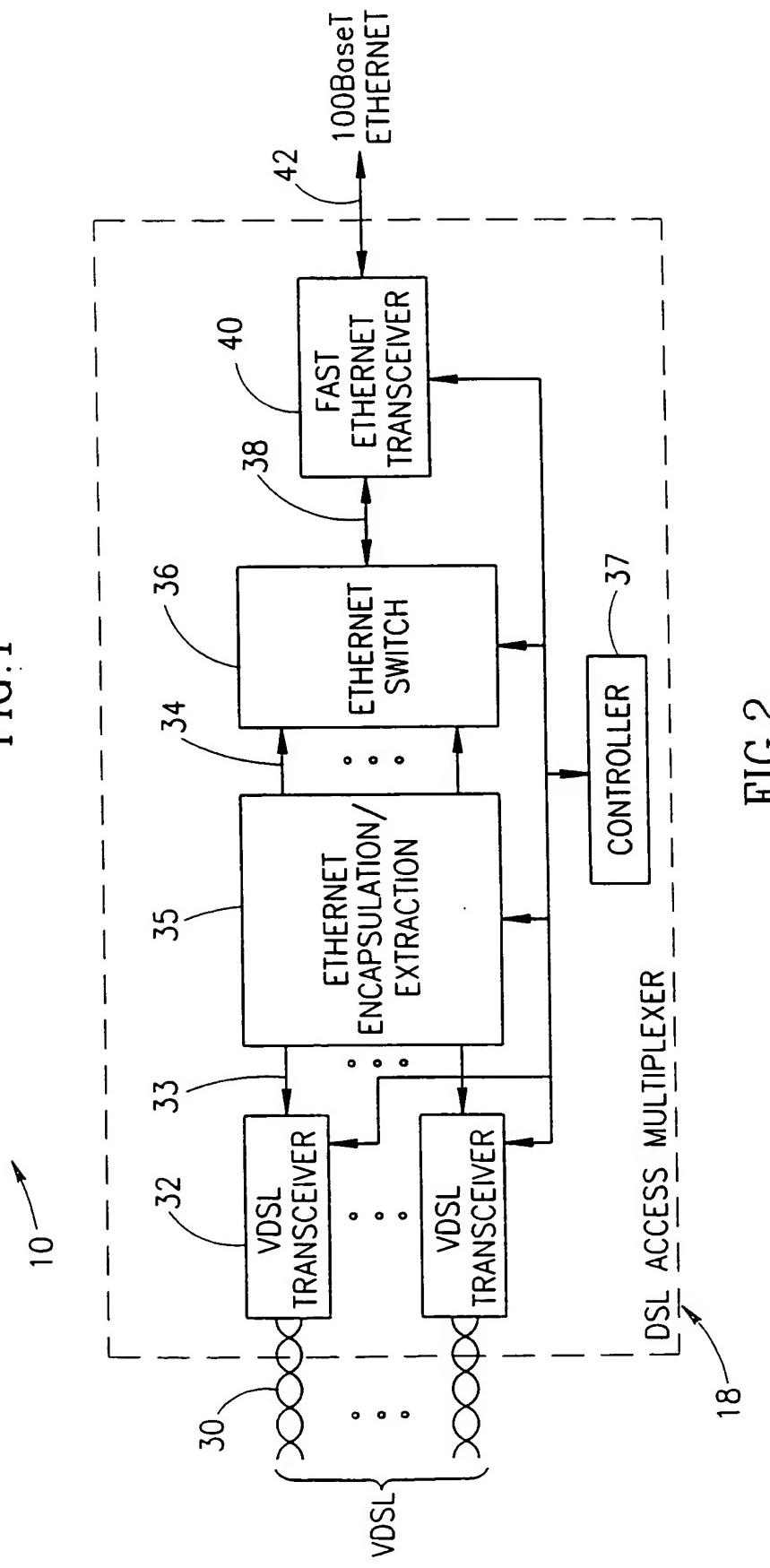
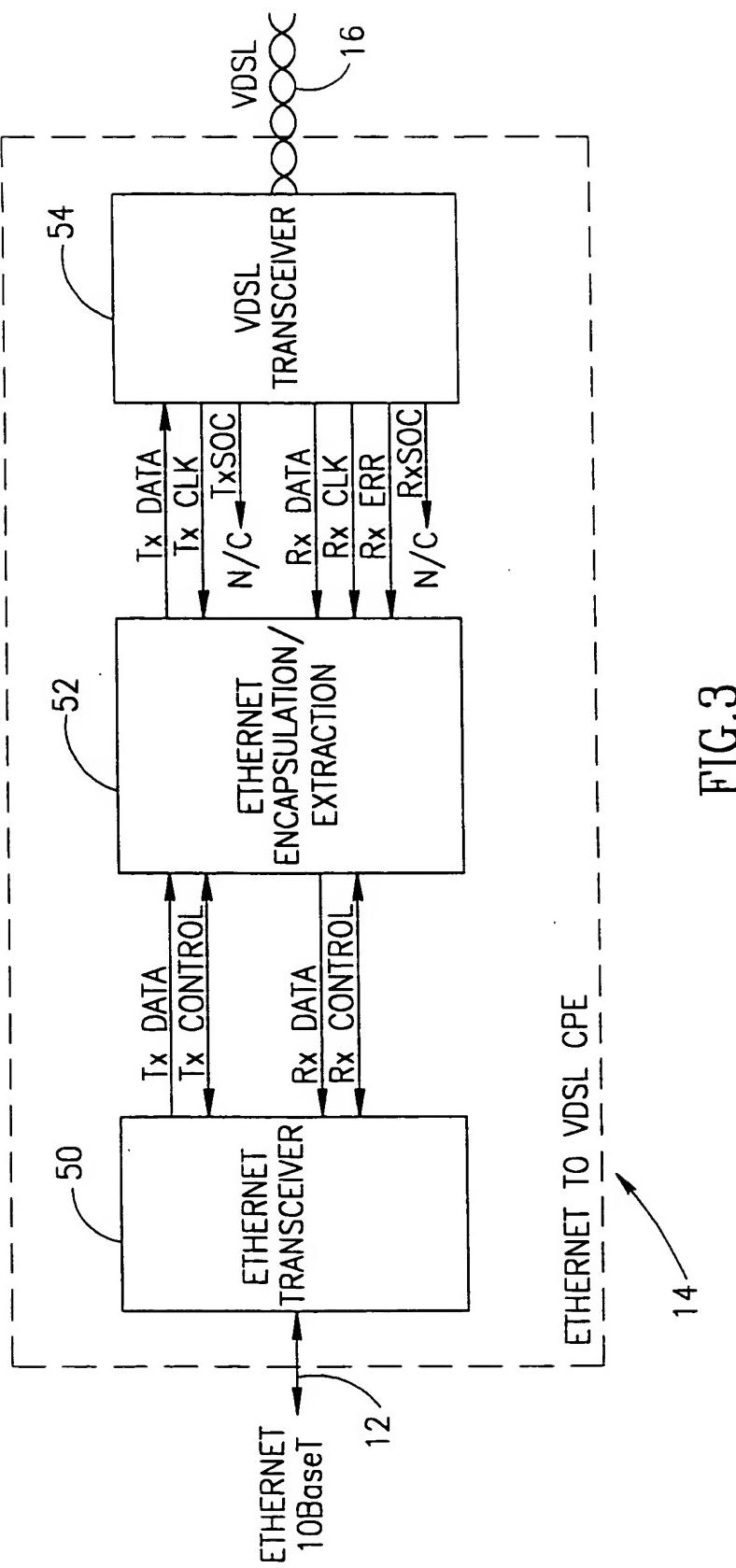


FIG. 1





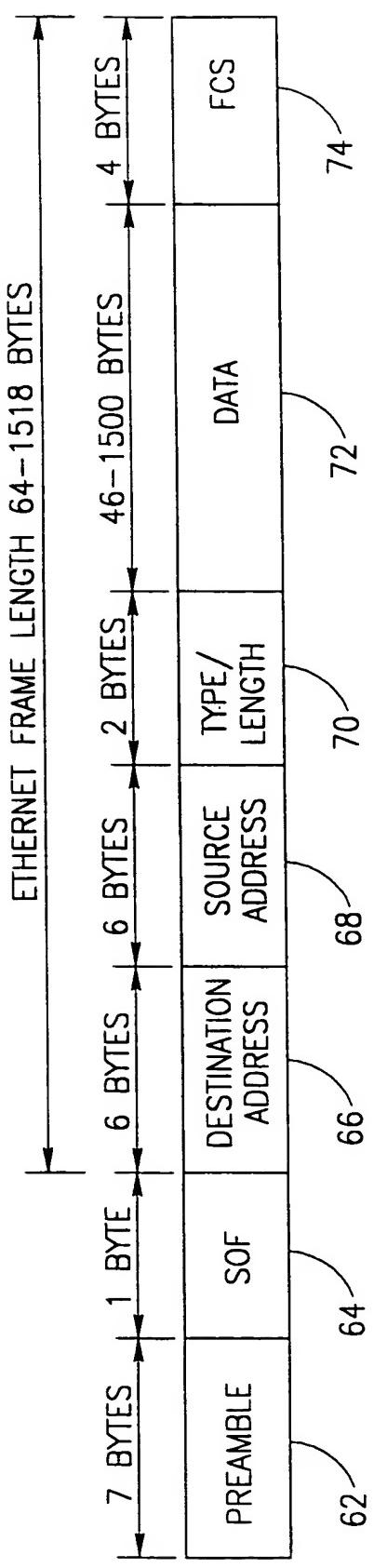


FIG. 4

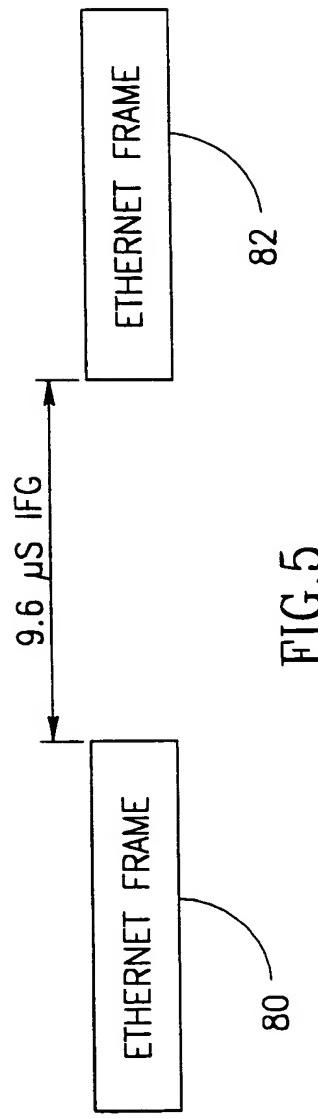


FIG. 5

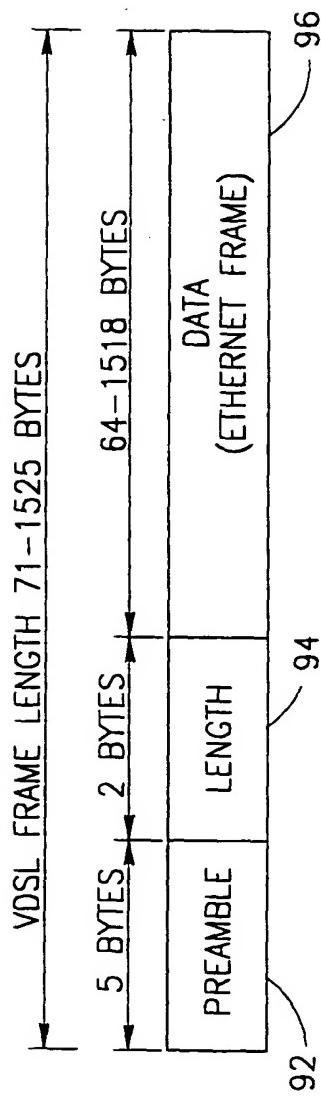


FIG.6
90

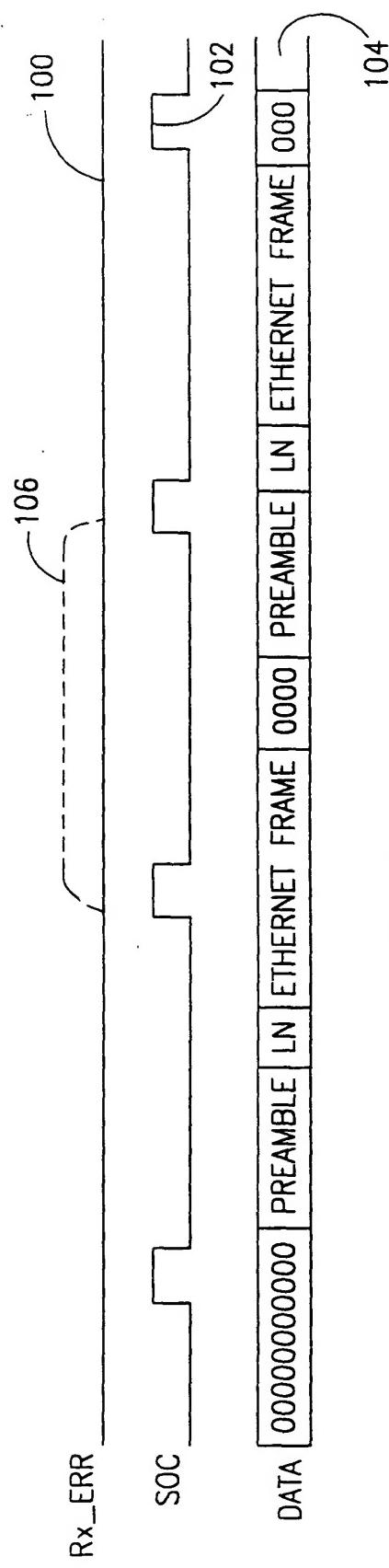


FIG.7

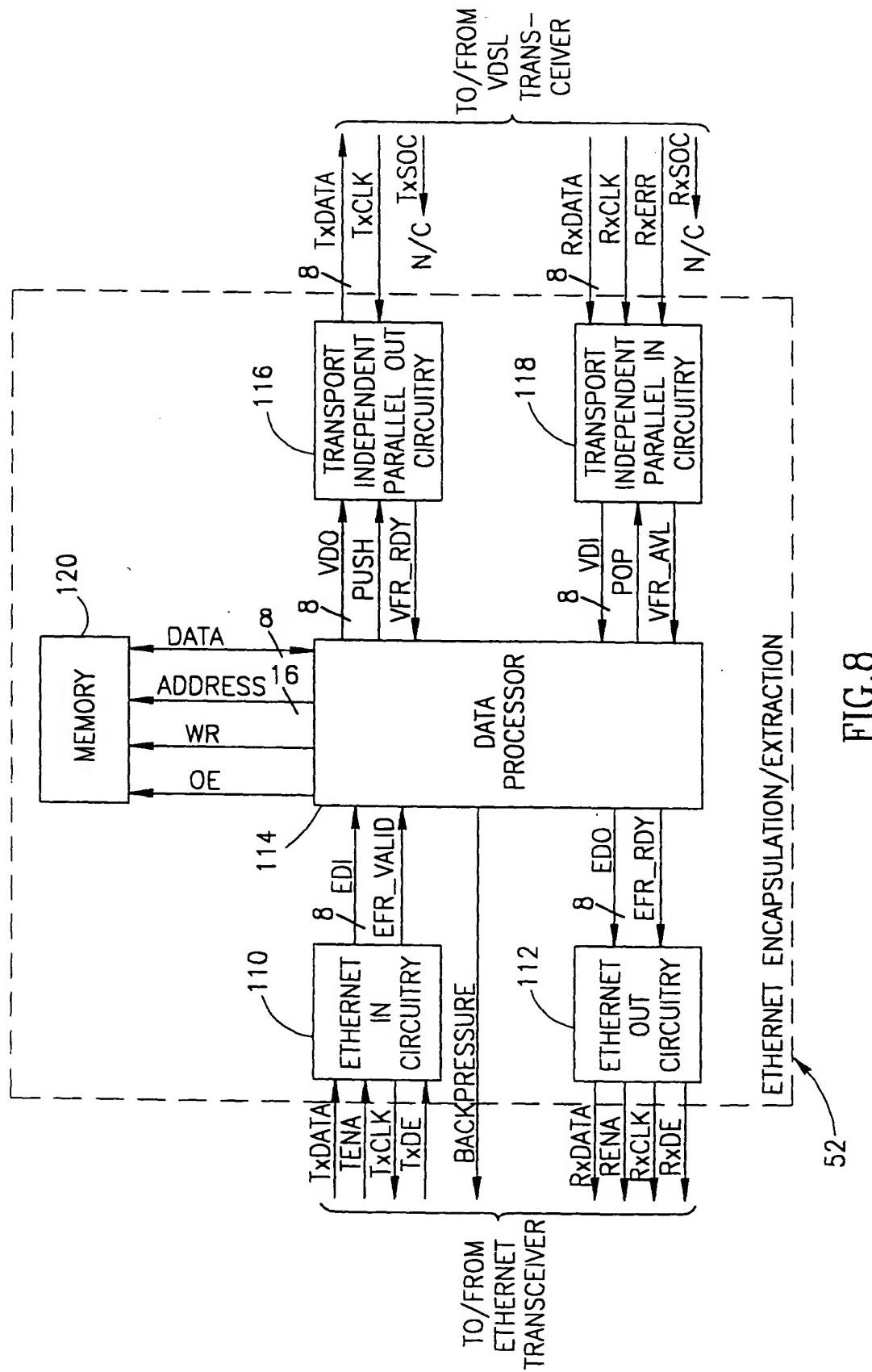


FIG. 8

ETHERNET IN CIRCUITRY
TO DATA PROCESSOR

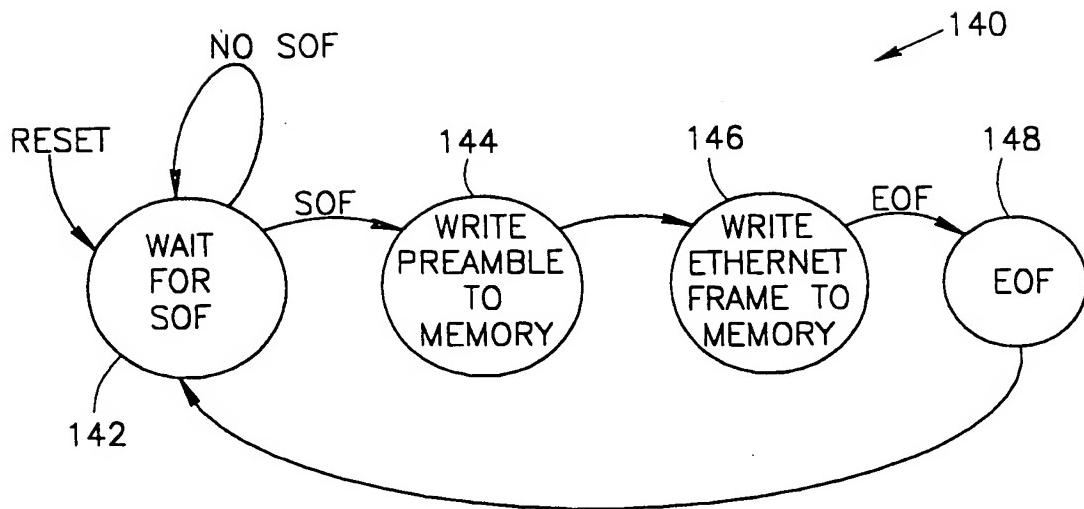


FIG.9

DATA PROCESSOR
TO TIPO CIRCUITRY

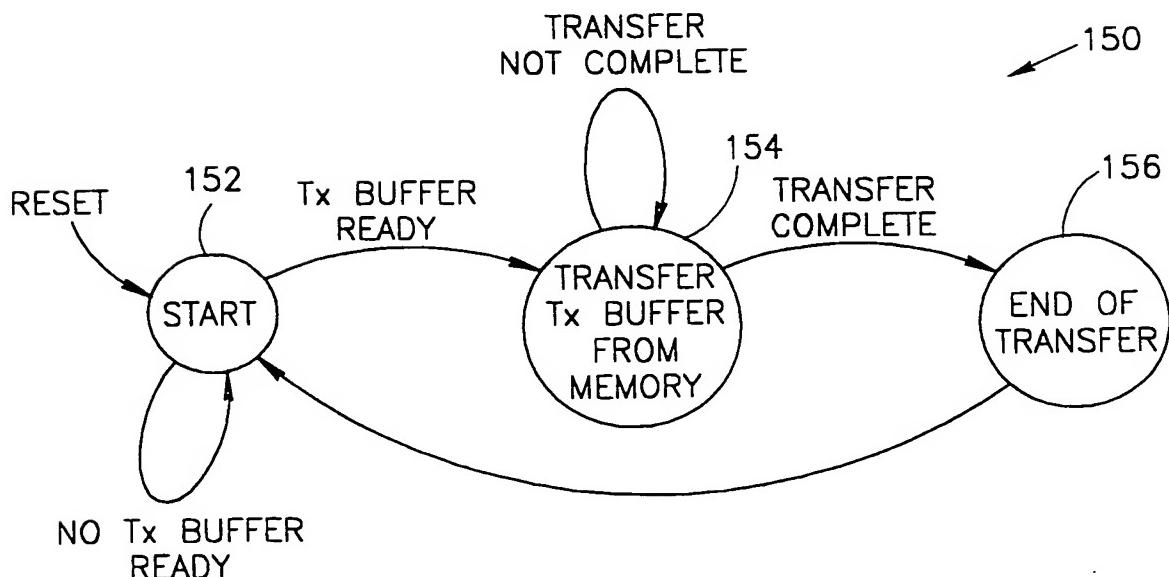


FIG.10

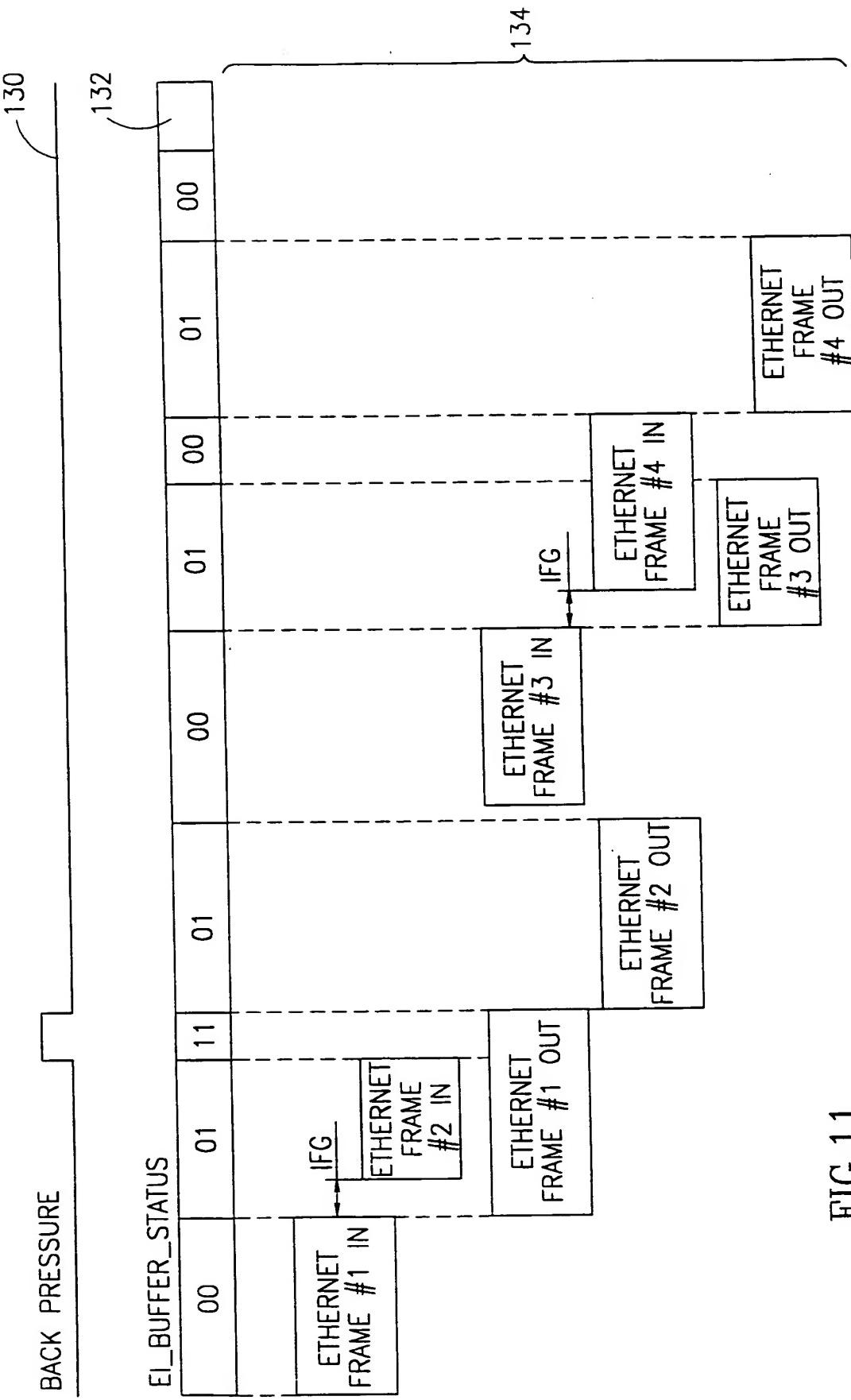


FIG. 11

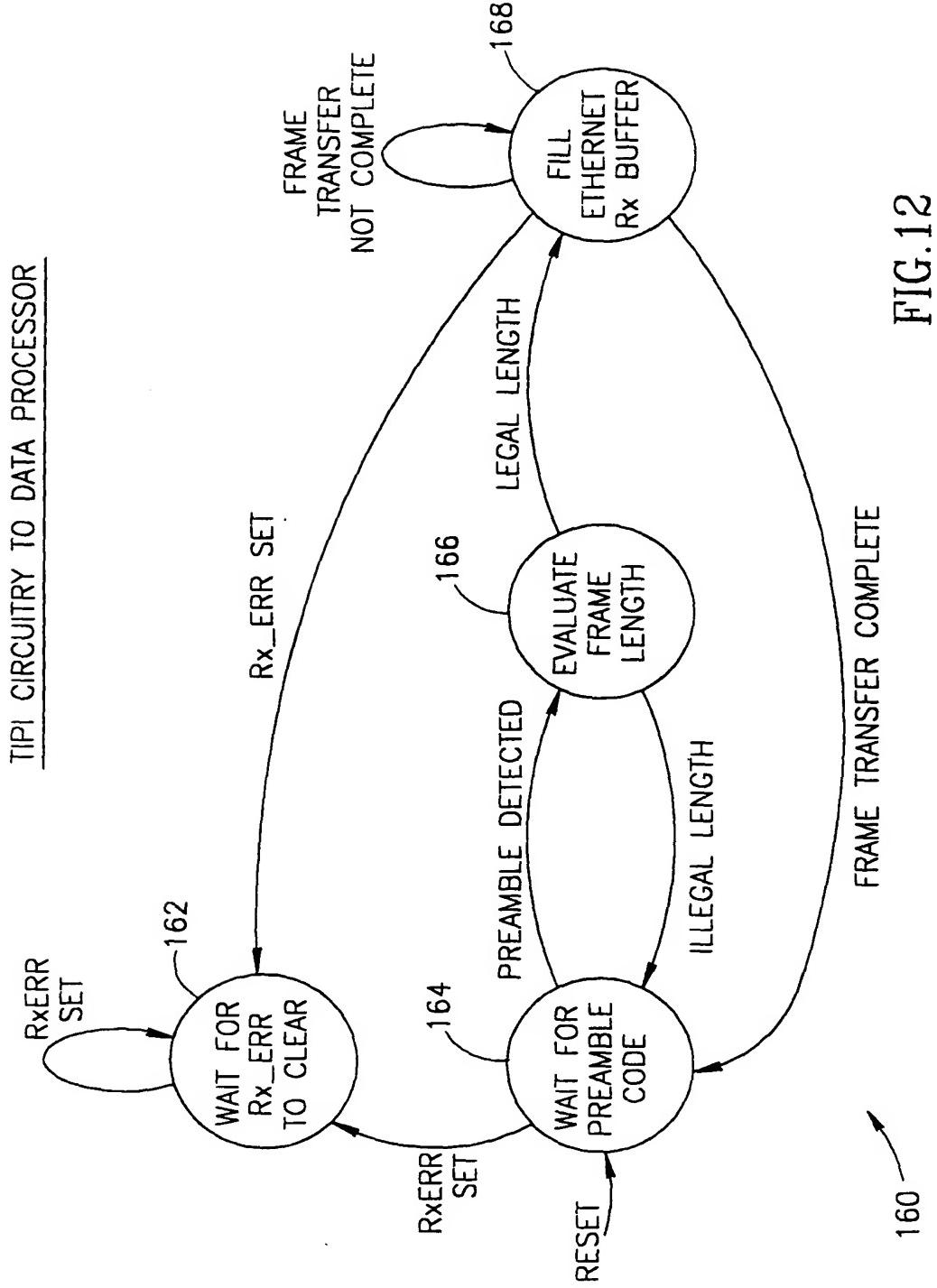


FIG.12

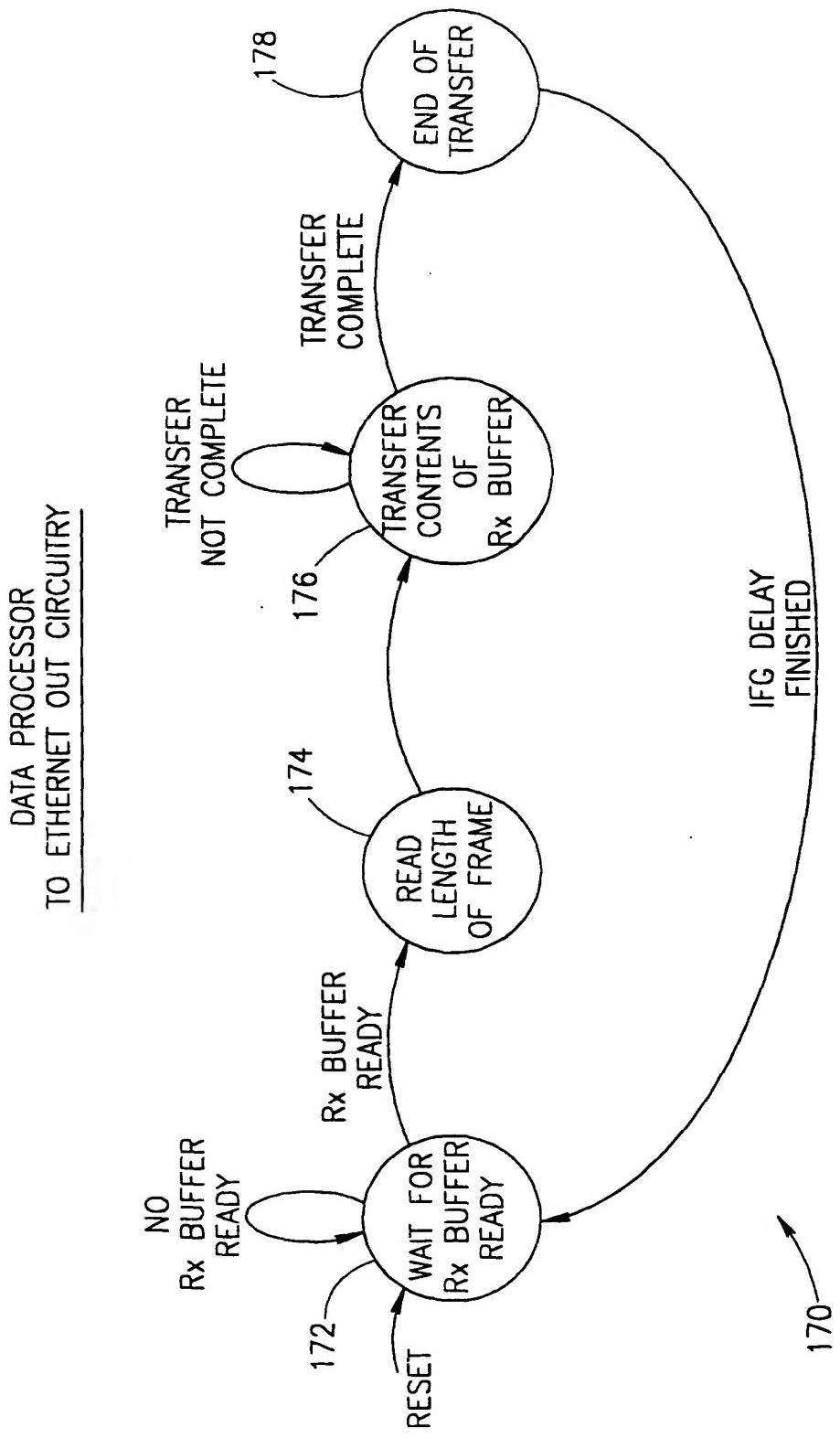


FIG.13